Aufteilung Team 4

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| --- | --- |
| adder\_subcomponents | Robert Markl |
| adder\_unit | Robert Markl |
| alu | Maximilian Ehm, Alex Popp, Robert Markl |
| bootloader\_defs\_pack | Robert Markl |
| bootloader\_program\_pack | Robert Markl |
| controller | Robert Markl |
| cpu | Maximilian Ehm |
| cpu\_defs\_pack | Maximilian Ehm, Robert Markl |
| cpu\_subprogram\_pack | Maximilian Ehm |
| datapath | Maximilian Ehm |
| fsm | Robert Markl |
| inc | Maximilian Ehm |
| InDevice | Orestis Alpos |
| instruction\_decoder | Maximilian Ehm |
| logic\_unit | Maximilian Ehm |
| memory | Alex Popp, Robert Markl, Maximilian Ehm |
| multiplexer | Alex Popp |
| OutDevice | Orestis Alpos |
| register | Alex Popp |
| shifter\_unit | Alex Popp |
| system | Maximilian Ehm |
| system\_behavioral | Robert Markl |
| system\_bootloader | Robert Markl |

# Testbenches

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| --- | --- |
| nop, stop | Robert Markl |
| add, addc | Maximilian Ehm |
| sub, subc | Orestis Alpos |
| not | Alexander Popp |
| and, or, xor | Alexander Popp |
| rea, reo, rex | Robert Markl |
| sll, srl, sra | Maximilian Ehm |
| rol, rolc, ror, rorc | Robert Markl |
| ldc, ldd, ldr | Orestis Alpos |
| std, str | Orestis Alpos |
| in, out | Maximilian Ehm |
| ldpc, stpc | Alexander Popp |
| jmp | Alexander Popp |
| jz, jc | Alexander Popp |
| jn, jo | Orestis Alpos |
| jnz, jnc, jnn | Maximilian Ehm |
| jno | Robert Markl |